

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S14	4	"5117205" and threshold	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/27 14:04
S15	214	( native adj transistor)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/27 14:28
S16	6	( native adj transistor).ti.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/28 10:06
S17	4	S16 and native and threshold	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/27 14:08
S18	3	Wfet	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/27 14:08
S19	2	( native adj transistor) with conventional adj transistor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/27 14:29
S20	5	native adj2 (fet mosfet transistor) with conventional adj2 ( fet mosfet transistor )	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/27 14:31
S21	119	native adj2 device with ( fet mosfet transistor )	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/27 14:32
S22	94	native adj2 device near4 ( fet mosfet transistor )	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/27 14:32
S23	28	( native and ( fet mosfet transistor)) .ti.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/28 10:08

S24	10	( native and ( fet mosfet transistor)) with ( transconductance gmc)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/28 10:11
S25	10	( native and ( fet mosfet transistor)) with ( transconductance gm adj cell)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/28 10:11
S26	10	( native and ( fet mosfet transistor)) with ( transconduct\$5 gm adj cell)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/28 10:12
S27	79	( native and ( fet mosfet transistor)) same ( transconduct\$5 gm adj cell oscillator filter)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/28 10:13
S28	22	( native adj ( fet mosfet transistor)) same ( transconduct\$5 gm adj cell oscillator filter)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/28 10:54
S29	5	( native adj2 ( fet mos mosfet transistor)) and transconduct\$5 and variable adj resist\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/28 11:05
S30	5	( native adj2 ( fet mos mosfet transistor)) and transconduct\$5 and variable adj2 resist\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/28 11:06
S31	11	( native adj2 ( fet mos mosfet device transistor)) and transconduct\$5 and variable adj2 resist\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/28 11:08
S32	6	S31 not S30	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/28 11:11

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	124	( vco oscillator) and ( transconduct\$4 gm ) and invert\$4 and ( noninvert\$ non adj invert\$4 non\$invert\$4) and capacit\$4 and filter\$4 and variable adj3 resist\$4 and ( transistor mos mosfet)	USPAT	OR	ON	2005/09/28 13:32
L2	0	1 and native	USPAT	OR	ON	2005/09/28 13:33
L3	2	( vco oscillator) and ( transconduct\$4 gm ) and invert\$4 and ( noninvert\$ non adj invert\$4 non\$invert\$4) and capacit\$4 and filter\$4 and variable adj3 resist\$4 and ( transistor mos mosfet) .ti	USPAT	OR	ON	2005/09/28 13:32
L4	0	(( vco oscillator) and ( transconduct\$4 gm ) and invert\$4 and ( noninvert\$ non adj invert\$4 non\$invert\$4) and capacit\$4 and filter\$4 and variable adj3 resist\$4 and ( transistor mos mosfet) ).ti.	USPAT	OR	ON	2005/09/28 13:32
L5	175	( vco oscillator) and ( transconduct\$4 gm ) and invert\$4 and ( noninvert\$ non adj invert\$4 non\$invert\$4) and capacit\$4 and filter\$4 and variable adj3 resist\$4 and ( transistor mos mosfet)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/28 13:33
L6	0	5 and native	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/28 13:33
L7	0	(( vco oscillator) and ( transconduct\$4 gm ) and invert\$4 and ( noninvert\$ non adj invert\$4 non\$invert\$4) and capacit\$4 and filter\$4 and variable adj3 resist\$4 and ( transistor mos mosfet) ).clm	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/28 13:33